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PRO

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10064518	07/23/2002	365	11503	2818	Ho, HCA

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**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO	
35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		MXIP0088USA
Verified and Acknowledged Examiner's initials			
TITLE : Method for programming, reading, and erasing a non-volatile memory with multi-level output currents			

U.S. DEPT. OF COMM. PAT. & TM PTO-426 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.	Print Fig.
		Primary Examiner		
		Application Examiner		
TERMINAL DISCLAIMER		PREPARED FOR ISSUE		
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